

Amendments to the Claims:

A listing of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121. This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Previously presented) A circuit arrangement for an AC voltage supply of a plasma display panel, the arrangement comprising:

 a transistor bridge having a pair of voltage input nodes and a pair of voltage output nodes,

 an input voltage coupled to the pair of voltage input nodes of the transistor bridge,

 a capacitor of a plasma cell coupled to the pair of voltage output nodes of the transistor bridge,

 a DC voltage converter that provides an auxiliary charging voltage from the input voltage, and

 a charging current circuit that receives the auxiliary charging voltage and provides charging current to the capacitor.

2. (Previously presented) The circuit arrangement of claim 1, wherein the DC voltage converter is a boost converter.

3. (Previously presented) The circuit arrangement of claim 2, wherein the boost converter comprises a supply transistor, a supply diode and a supply inductor.

4. (Previously presented) The circuit arrangement of claim 3, wherein

 the DC voltage converter provides the auxiliary charging voltage to a charging capacitor, and an auxiliary discharging voltage to a discharge capacitor, and

 the inductor and diode are arranged in series between the charging capacitor and discharging capacitor.

5. (Previously presented) The circuit arrangement of claim 1, wherein the auxiliary charging voltage is greater than half the input voltage.

6. (Previously presented) The circuit arrangement of claim 1, wherein the charging current circuit includes a series combination of a charging transistor, a charging diode and a charging inductor.

7. (Previously presented) The circuit arrangement of claim 1, wherein the auxiliary charging voltage is applied to an auxiliary capacitor.

8. (Currently amended) The circuit arrangement of claim ~~1~~7, wherein a capacitance of the auxiliary capacitor is much larger than a capacitance of the capacitor of the plasma cell.

9. (Previously presented) The circuit arrangement of claim 1, wherein the DC voltage converter provides an auxiliary discharging voltage, and the auxiliary charging voltage is generated from the auxiliary discharging voltage.

10. (Previously presented) The circuit arrangement of claim 3, wherein the supply transistor shares a first connection point with an auxiliary charging capacitance that stores the auxiliary voltage and a ground terminal of the input voltage, and a second connection point shared with the supply inductor and an anode of the supply diode.

11. (Previously presented) The circuit arrangement of claim 10, wherein a cathode of the supply diode has a connection point shared with a charging transistor of the charging current circuit and the auxiliary charging capacitor.

12. (Previously presented) The circuit arrangement of claim 10, wherein the supply inductor is connected at least to a discharging transistor of a discharging current circuit.

13. (Previously presented) The circuit arrangement of claim 3, wherein the auxiliary charging voltage is greater than half the input voltage.

14. (Currently amended) A circuit arrangement for the ~~supplying AC voltage supply of~~ to a plasma display panel, the arrangement comprising:

- a transistor bridge having a pair of voltage input nodes and a pair of voltage output nodes,

- an input voltage coupled to the pair of voltage input nodes of the transistor bridge,

- a capacitor of a plasma cell coupled to the pair of voltage output nodes of the transistor bridge,

- a DC voltage converter that provides an auxiliary discharging voltage from the input voltage, and

- a discharging circuit that receives the auxiliary discharging voltage and provides discharging current to the capacitor.

15. (Previously presented) The circuit arrangement of claim 14, wherein the DC voltage converter is a buck converter.

16. (Previously presented) The circuit arrangement of claim 15, wherein the buck converter comprises a supply transistor, a supply diode and a supply inductor.

17. (Previously presented) The circuit arrangement of claim 15, wherein the buck converter is connected to a positive side of the input voltage, a negative side of the input voltage, and to an auxiliary discharge capacitor that stores the discharge voltage.

18. (Previously presented) The circuit arrangement of claim 14, wherein the auxiliary discharging voltage is less than half the input voltage.

19. (Previously presented) The circuit arrangement of claim 14, wherein the discharging circuit includes a series combination of a discharging transistor, a discharging diode and a discharging inductor.

20. (Previously presented) The circuit arrangement of claim 14, wherein the auxiliary discharging voltage is applied to an auxiliary discharging capacitor.

21. (Previously presented) The circuit arrangement of claim 20, wherein a capacitance of the auxiliary discharging capacitor is significantly greater than a capacitance of the plasma cell.

22. (Previously presented) The circuit arrangement of claim 14, wherein the auxiliary discharging voltage is generated from a discharge of the capacitor of the plasma cell and stabilized by the DC voltage converter.

23. (Previously presented) The circuit arrangement of claim 22, wherein the DC voltage converter compensates for losses caused by commutation and takes power from the input voltage.

24. (Previously presented) The circuit arrangement of claim 16, wherein the supply transistor shares a first common connection point with a positive side of the input voltage and shares a common connection point with the supply inductor and an anode of the supply diode.

25. (Previously presented) The circuit arrangement of claim 16, wherein the supply inductor is connected to a discharging transistor of the discharging circuit.

26. (Previously presented) The circuit arrangement of claim 25, wherein the supply inductor is connected at least to a charging transistor of a charging circuit.

27. (Previously presented) The circuit arrangement of claim 21, wherein the auxiliary discharging voltage is less than half the input voltage.

28. (Previously presented) The circuit arrangement of claim 1, wherein the DC voltage converter provides an auxiliary discharging voltage from the input voltage, and the auxiliary charging and discharging voltages are used for a plurality of independent bridge circuits that are coupled to the input voltage.

29. (Currently amended) A plasma display panel comprising a circuit arrangement for supplying AC voltage to the plasma display panel, ~~which the~~ the circuit arrangement including comprises:

- a transistor bridge having a pair of voltage input nodes and a pair of voltage output nodes,

- an input voltage coupled to the pair of voltage input nodes of the transistor bridge,

- a capacitor of a plasma cell coupled to the pair of voltage output nodes of the transistor bridge,

- a DC voltage converter that provides an auxiliary charging voltage from the input voltage, and

- a charging circuit that receives the auxiliary charging voltage and provides charging current to the capacitor.